ABSTRACT OF THE DISCLOSURE

For a pseudo-SRAM (Static Random Access Memory) macro operating in synchronization with a clock signal, a page operation instructing signal instructing a page operation and a page close instructing signal instructing completion of the page operation are prepared as control signals designating operation modes. A pseudo-SRAM can be selectively operated in a page mode in accordance with the page operation instructing signal and the page close instructing signal, and an operation of row-related circuitry in each clock cycle can be inhibited so that an average power consumption can be reduced. The power consumption of the pseudo-SRAM can be reduced without lowering an operation speed.

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